Claims

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What is claimed is:

1.	A fiber optic transceiver array for short wave fiber optic
communicati	ons comprising:

a series of fiber optic transceiver channels; each fiber optic transceiver channel including a plurality of test pads;

a power distribution bypass capacitor distributed along said series of fiber optic transceiver channels;

a plurality of high voltage power supply and ground connections coupled through said power distribution bypass capacitor and distributed around said series of fiber optic transceiver channels;

a threaded high voltage power supply connection extending to alternating ones of said series of fiber optic transceiver channels.

- 2. A fiber optic transceiver array for short wave fiber optic communications as recited in claim 1 includes a threaded ground connection extending to alternating other ones of said series of fiber optic transceiver channels, said alternating other ones of said series of fiber optic transceiver channels not including said threaded high voltage power supply connection.
- 3. A fiber optic transceiver array for short wave fiber optic communications as recited in claim 1 wherein said plurality of test pads of each fiber optic transceiver channel includes a ground connection and a pair of differential output connections.
- 4. A fiber optic transceiver array for short wave fiber optic communications as recited in claim 3 wherein said threaded high voltage power supply connection extending to alternating ones of said series of fiber optic transceiver channels includes said threaded high voltage power supply connection extending between said ground connection and one of said pair of differential output connections.

_	A fiber optic transceiver array for short wave fiber optic
5.	A liber optic transceiver array for short wave liber optic
communications as recited in claim 3 includes a threaded ground connection	
extending to alternating other ones of said series of fiber optic transceiver	
channels to provide alternating low impedance ground and low impedance	
high voltage power supply channels in said series of fiber optic transceiver	
channels.	

- 6. A fiber optic transceiver array for short wave fiber optic communications as recited in claim 1 wherein each said fiber optic transceiver channel includes a channel decoupling capacitor positioned proximate to said pair of differential output connections.
- 7. A fiber optic transceiver array for short wave fiber optic communications as recited in claim 1 wherein said each said fiber optic transceiver channel includes a photodetector and said series of fiber optic transceiver channels have a predefined spacing between sequential ones of said photodetectors.
- 8. A fiber optic transceiver channel for short wave fiber optic communications comprising:

at least a pair of high voltage power supply and ground connections;

a plurality of test pads including at least a ground connection and a pair of differential output connections; and

a channel decoupling capacitor positioned proximate to said pair of differential output connections.

- 9. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 8 wherein said pair of high voltage power supply and ground connections includes a low impedance ground connection coupled through a power distribution bypass capacitor.
- 10. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 8 wherein said pair of high voltage power supply and ground connections includes a low impedance high voltage power supply connection coupled through a power distribution bypass capacitor.

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- 1 11. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 8 includes a photodetector.
 - 12. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 8 includes an output buffer circuitry coupled to said differential output connections.
 - 13. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 12 wherein said output buffer circuitry includes an input stage including a differential pair of transistors.
 - 14. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 13 wherein said differential pair includes a differential pair of high gain metal semiconductor field effect transistors (MESFETs).
 - 15. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 13 includes a first source follower transistor pair coupled to said differential pair of transistors.
 - 16. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 15 includes a second source follower transistor pair coupled to said first source follower transistor pair.
 - 17. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 16 wherein said second source follower pair is connected to said differential output connections.
 - 18. A fiber optic transceiver channel for short wave fiber optic communications as recited in claim 16 wherein said first and second source follower pairs provide unity gain.